

## BRIEF BIO DATA

1.	<b>Name</b>	Vikash Sharma		
2	<b>Designation</b>	Assistant Professor		
3.	<b>Date of Birth</b>	15 July 1990		
4.	<b>Nationality</b>	Indian		
5.	<b>Postal Address</b>	Department of Electronics & Communication, Rewa Engineering College, Rewa (M.P.) 486001		
6.	<b>Contact Details</b>	9074493296		
7.	<b>Area of Specialization</b>	Nano electronic Devices: Modeling and Simulation, Low Power VLSI Design		
8	<b>Total Experience</b>	<b>Teaching</b>	<b>Industrial</b>	<b>Total</b>
		05 YEA RS		05
9	<b>Technical Qualification:</b>	<b>Qualification</b>	<b>Institute and Year</b>	
		<b>Ph.D</b>		
		<b>M. Tech</b>	ABV Indian Institute of Information Technology & Management Gwalior (M.P.)	
		<b>BE/B.Tech</b>	Madhav Institute of Technology & Science Gwalior	
10	<b>Membership of Professional Bodies</b>			
11	<b>Workshop/Training program/Summer/Winter School etc. attended</b>	06		
12	<b>Workshop/Training program/Summer/Winter School etc. Organised</b>			
13	<b>Conference/ Seminar Attended</b>	03		
14	<b>Conference/ Seminar Organised</b>			
15	<b>Research Publication</b>	<b>International /National Conference</b>	<b>International /National Journal</b>	<b>Book</b>
		02	02	
16	<b>UG Projects/Thesis Guided</b>	<b>UG Projects</b>	<b>M. Tech</b>	<b>Ph. D</b>
		15		
17	<b>Patents</b>			
18	<b>Consultancy Project/Research Project</b>			
19	<b>Other Achievements/Award</b>	Qualified UGC NET EXAM, QUALIFIED GATE EXAM SEVERAL TIMES		
20	<b>Other Information</b>			

I Vikash Sharma hereby declare that the details provided above are true and best of my knowledge.

